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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group: Unknown
Attorney
Docket. 5727-65832
Applicant. Zindel Herbert Heller
Invention. INSTRUMENT
Serial No: 10/046,030
Filed: November 7, 2001
Examiner: Unknown

Certificate Under 37 CFR 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231

on February 5, 2002

Kim Tyree
Kim Tyree

Dated: February 5, 2002

LETTER TO OFFICIAL DRAFTSMAN
SUBMISSION OF ACCEPTABLE DRAWINGS

Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicant submits herewith seven sheets of acceptable drawings (Figs. 1-11) for the above-identified application.

Accordingly, Applicant submits that this application is condition for grant. Such action is respectfully requested.

Respectfully submitted,

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INDS02 RDC427588v1

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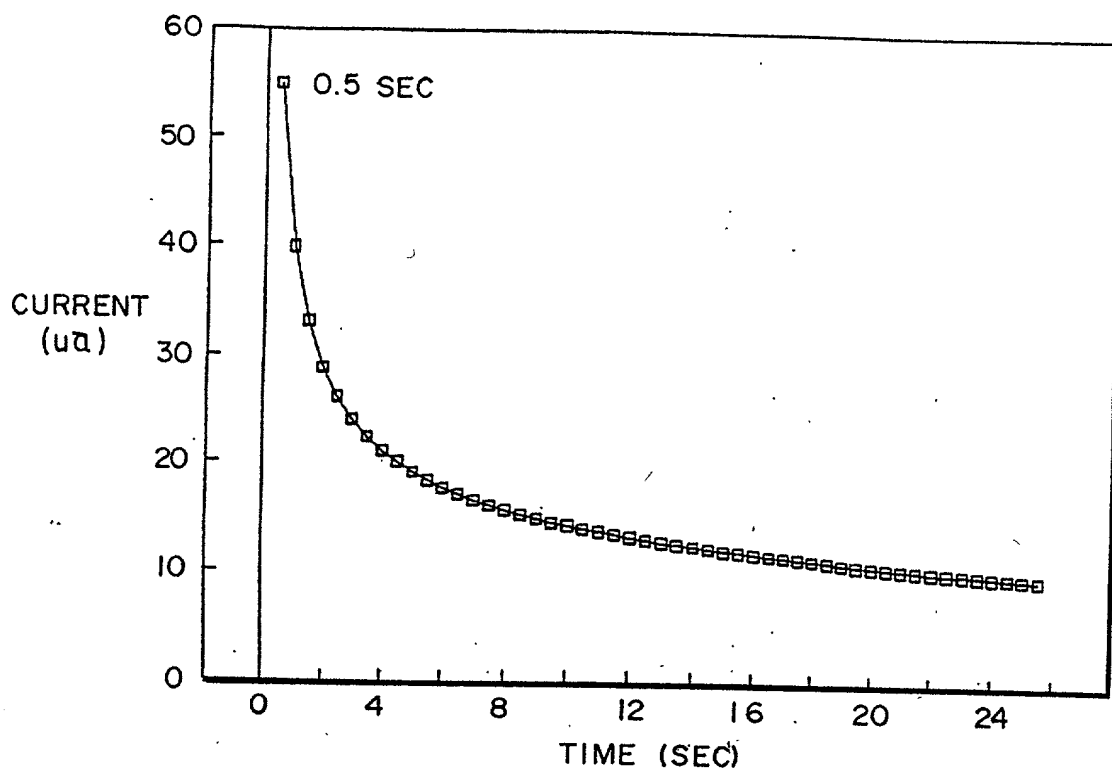


FIG. 1

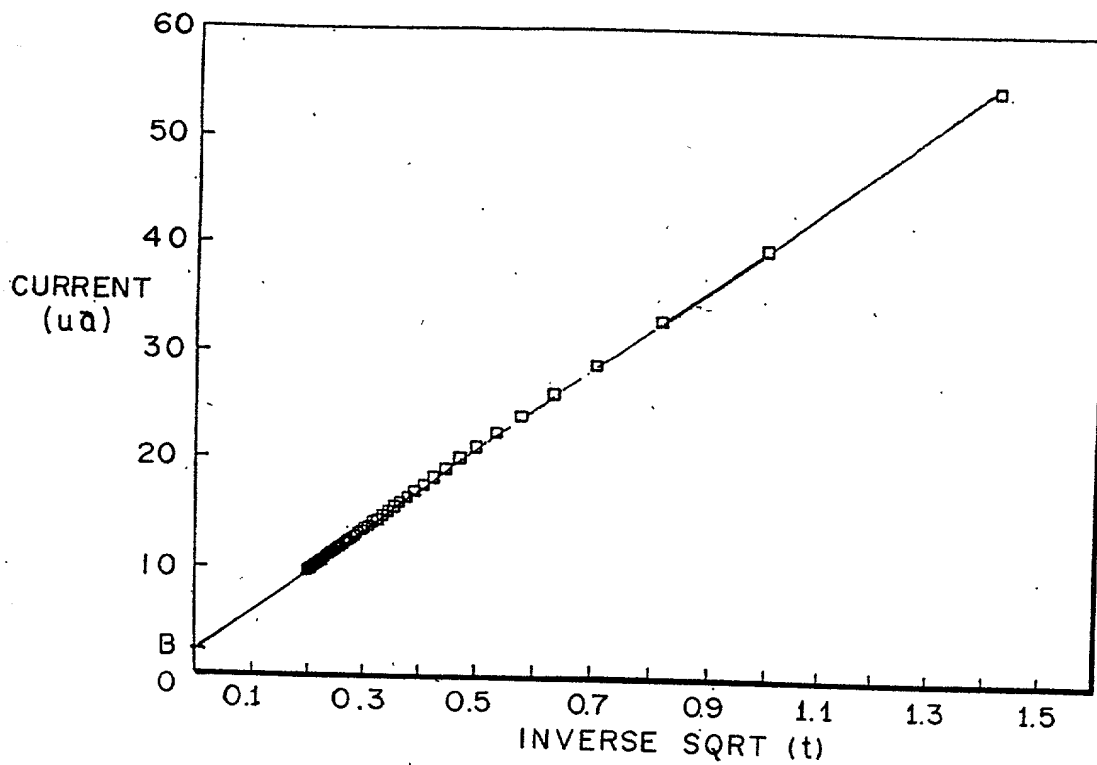


FIG. 2

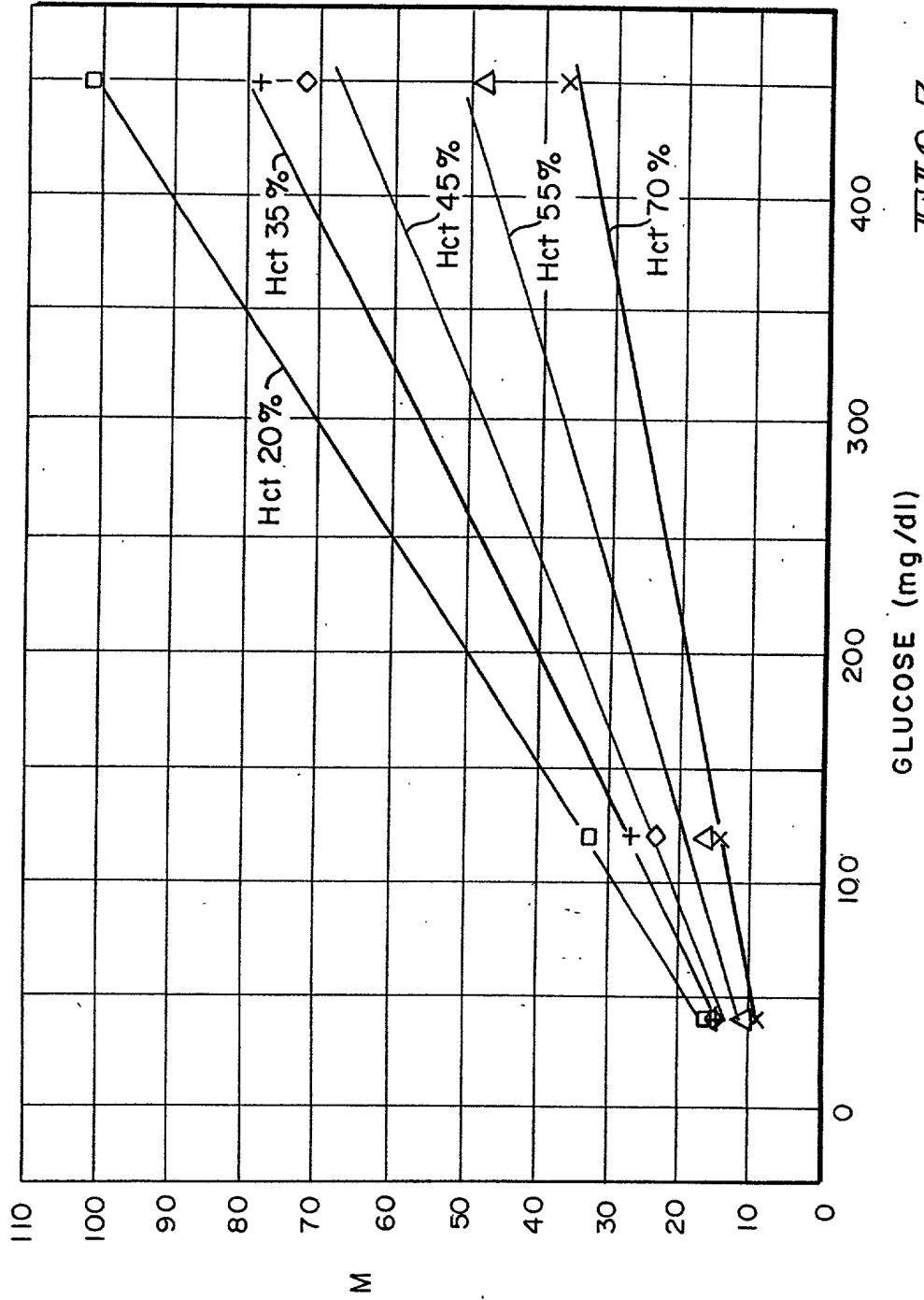


FIG 3

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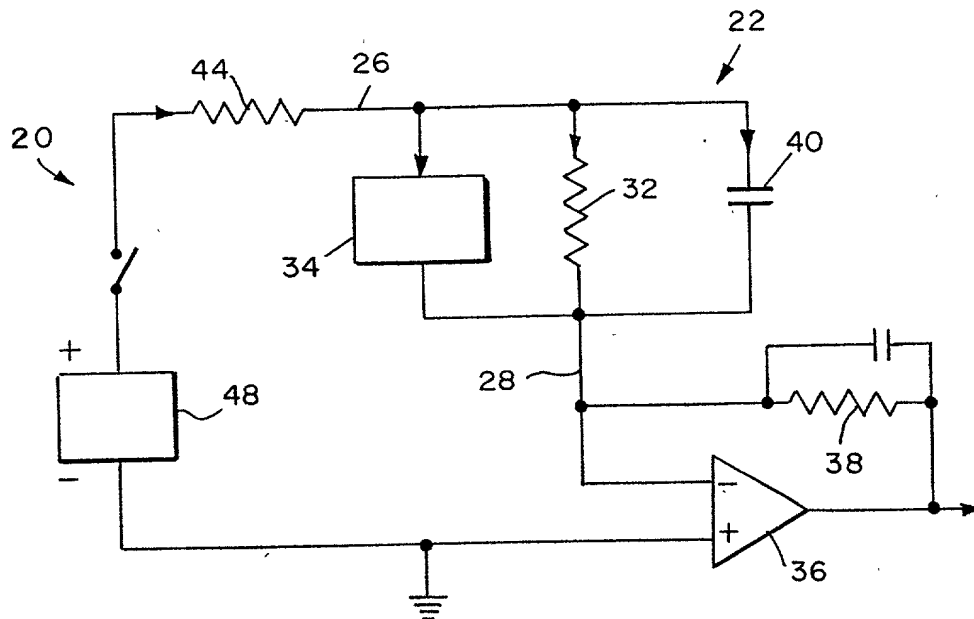


FIG 4

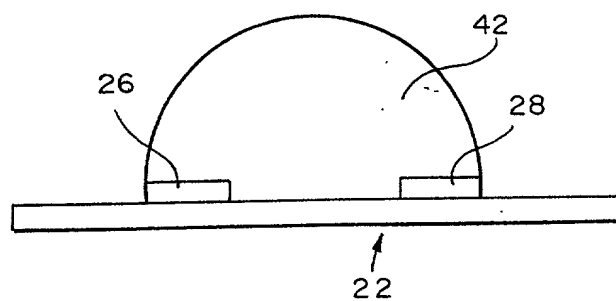


FIG 5

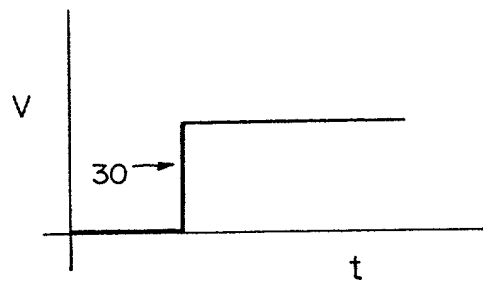


FIG. 6

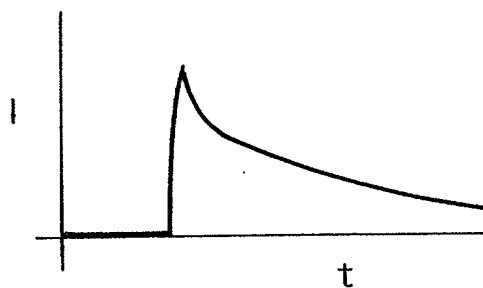


FIG. 7

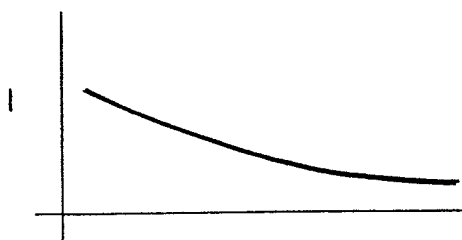
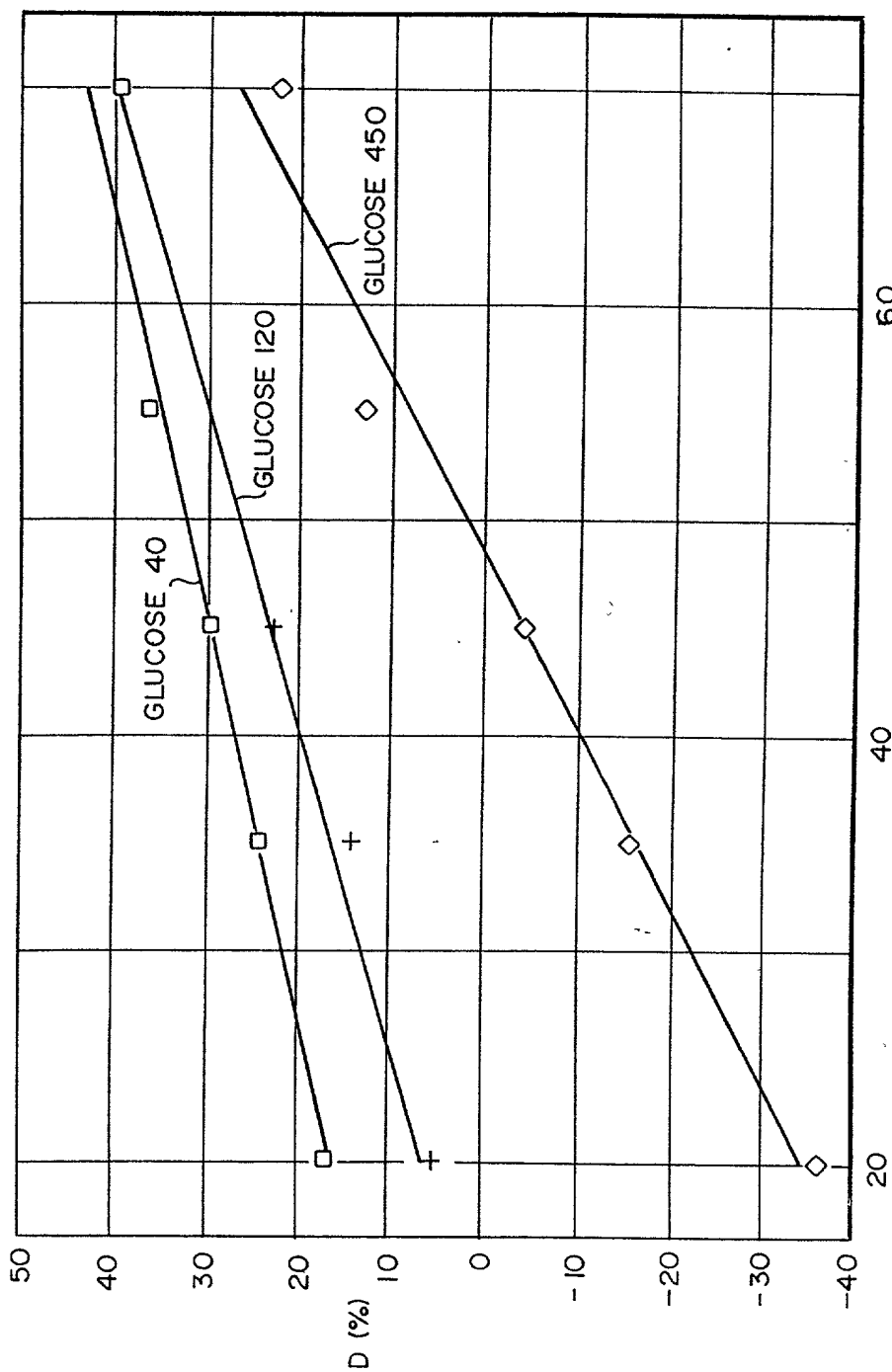


FIG. 8



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FIG. 9

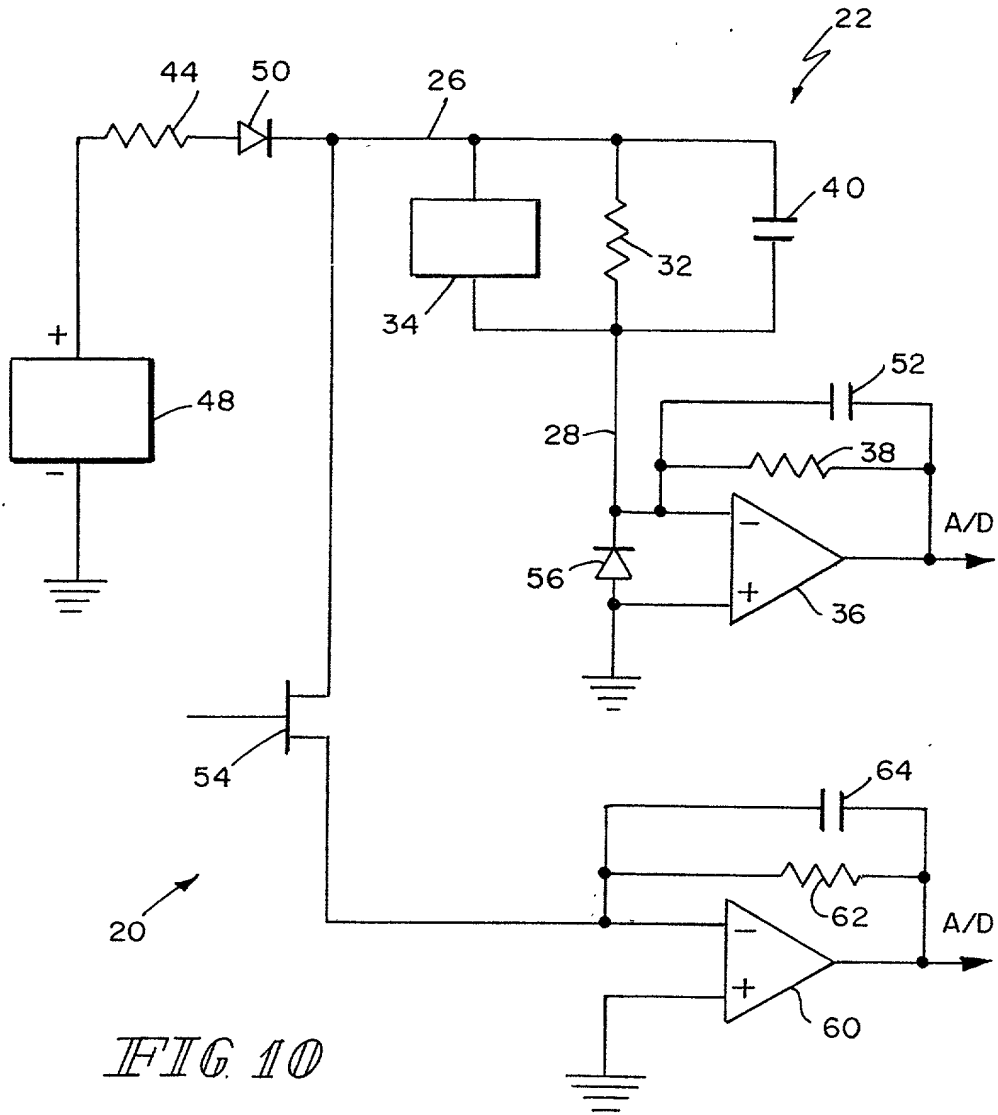


FIG 10

The circuit diagram illustrates a differential amplifier system for measuring the resistance of a variable resistor 20. A voltage source 48 is connected in series with a resistor 44 and a diode 50. The output of this series combination is connected to a node that branches into two paths. One path leads to a resistor 72, which is connected to a diode 74, and then to a node 28. The other path leads to a node 54, which is connected to the base of a transistor 56. The emitter of transistor 56 is grounded. The collector of transistor 56 is connected to a node 26, which is also connected to a resistor 70. The other end of resistor 70 is connected to a node 22, which is also connected to a resistor 72. The output of the first differential amplifier stage is connected to an A/D converter 36. The second differential amplifier stage is connected to an A/D converter 60. The output of the second A/D converter is connected to a node 64, which is also connected to a resistor 62. The output of the second A/D converter is labeled A/D.

FIG. 11